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APPLICANT: NISSAN MOTOR CO LTD;

INVENTOR:

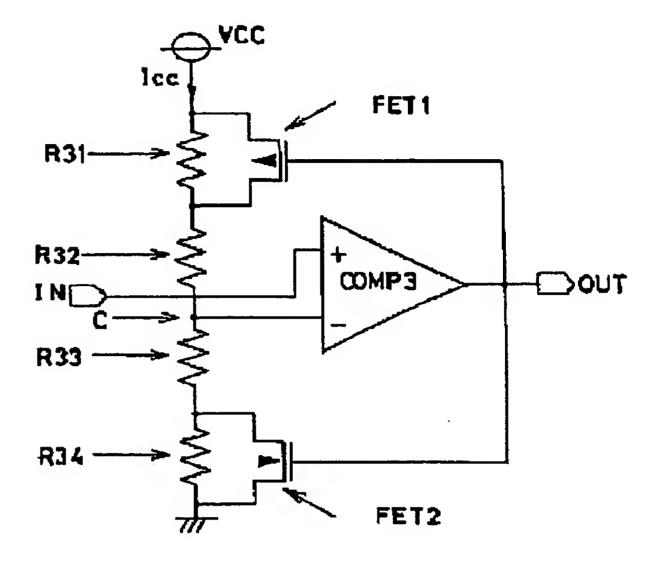
KIYOTA SHIGEYUKI;

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TITLE

HYSTERESIS COMPARATOR



## ABSTRACT:

PROBLEM TO BE SOLVED: To reduce electric power consumption by respectively connecting short-circuit means to a power source side resistance and a grounding side resistance of a reference power source generating circuit, short-circuiting the short-circuit means by a short-circuit signal from a comparator, and generating hysteresis.

SOLUTION: An FET 1 and an FET 2 being short-circuit means are respectively connected to both ends of a power source side resistance R31 and a grounding side resistance R34 of a reference voltage generating circuit, and both gates of the FETs 1 and 2 are connected to the output end of a comparator 3, By constituting these in this way, the FETs 1 and 2 mutually and inversely operate according to output of the comparator 3, and generate hysteresis by short-circuiting respective both ends of resistances 31 and 34. Therefore, a constant current source to generate the hysteresis is not required, and since there is no large change in a resistance value caused by generation of the hysteresis, electric power consumption can be reduced.

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